



SDR Embedded Software Engineer/Architect

Position Description

SSC is seeking highly motivated mid and senior level engineers who will lead tasks and teams developing next generation wireless networking and RF system software. You will develop radio solutions on SDR hardware for advanced wireless communications applications. Application implementations include basic functionality such as sensing, signal processing, wireless protocol implementation, and beamforming as well as advanced system capabilities that adapt transmit and receive behaviors in real time. You will participate in the entire product life cycle including architecture of the real time embedded system through test and evaluation support. Projects include applications to traditional Military and First Responder RF systems as well as adaptations of commercial (e.g., 5G) technologies. Successful Senior Engineer candidates will additionally provide mentoring and leadership to junior team members, and provide technical oversight for SDR development tasks and teams that include subcontractors and vendors.

Responsibilities

- Design and develop the architecture and embedded software solutions of innovative radio communications, sensing, and spectrum access systems
- Understand customer and/or project needs to implement effective SDR designs
- Interact with internal and external engineering teams on a regular basis to design and create interfaces with other system elements
- Provide technical and professional leadership and mentoring to junior team members

Experience Qualifications

- Experience with wireless networking and radio communications systems and strong understanding of embedded systems, hardware design, and signal processing concepts
- 5 to 10+ years demonstrated experience in architecting and developing complex embedded systems using FPGAs and in Linux
- Expertise in FPGA design and development
- Experience with common programming languages such as Python, C++, and C.
- Expertise with SDR platforms such as ADRV9361-Z7035 or Xilinx RFSoc, with exposure to Xilinx Zynq devices or UltraScale MPSoC and a deep understanding of the design flow and toolchain use.
- Experience implementing wireless technologies on a SDR such as with the PHY, MAC, TDMA, RADAR, detection, classification, 5G, LTE, beamforming, or MIMO.
- Experience leading projects and development teams
- Strong verbal and written communication skills

Education and Other Qualifications

- Required: US citizenship or Permanent Residency



- BS or MS (preferred) in Computer Science, Electrical Engineering, Computer Engineering, or related field
- DoD SECRET clearance preferred

We Offer

- Competitive pay and benefits package
- Medical insurance
- 401(k) plan
- Paid vacation and holidays
- Free parking
- Easily accessible to DC and VA (walking distance to Spring Hill Metro station)

The ideal candidate would live in the Northern Virginia/Greater Washington DC area.

To Apply

Email resume and letter of interest to jobs@sharedspectrum.com

Company Description

Shared Spectrum Company (SSC) is a pioneer in the development of innovative wireless advanced technologies that are revolutionizing the military and commercial use of spectrum. SSC has a unique technology development culture focused on technical excellence, innovation and execution. This is a unique opportunity to gain invaluable experience in one of the most important technological advancements in wireless communications technologies today. SSC is an Equal Opportunity Employer.